UTC 4124 NPN TRIPLE DIFFUSED SILICON TRANSISTOR

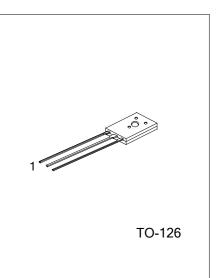
HIGH FREQUENCY SWITCHING TRANSISTORS FOR BALLASTERS

DESCRIPTION

UTC 4124 is designed for specially used for electronic ballasters in 110VAC environment.

FEATURES

- * Triple diffused technology.
- * High switching speed



1: BASE 2: COLLECTOR 3: EMITTER *Pb-free plating product number:4124L

ABSOLUTE MAXIMUM RATINGS

(Tc = 25℃)

PARAMETER	SYMBOL	RATINGS	UNIT
Collector-Base Voltage	V _{CBO}	400	V
Collector-Emitter Voltage	V _{CEO}	200	V
Collector-Emitter Voltage	wwwDataSheet4U.com	7	V
Peak Collector Current	Ι _C	1.5	А
Peak Collector Consume Dissipation	Pc	20	W
Peak Junction Temperature	TJ	150	°C
Storage Temperature	T _{STG}	-40 ~ +150	°C

ELECTRICAL CHARACTERISTICS

(Ta = 25℃)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Collector-Emitter Sustaining Voltage	V _{CEO (SUS)}	I _C =10mA, I _B =0	200			V
Collector-Base Breakdown Voltage	V (BR) CBO	I _C =1mA, I _B =0	400			V
Emitter-Base Breakdown Voltage	V (BR) EBO	I _E =1mA, I _C =0	7			V
Collector-Base Cutoff Current	I _{CBO}	V _{CB} =400V, I _E =0			100	μA
Collector-Emitter Cutoff Current	I _{CEO}	V _{CE} =200V, I _B =0			100	μA
Emitter-Base Cutoff Current	I _{EBO}	V _{EB} =7V, Ic=0			100	μA
DC Current Gain	h _{FE (1)}	V _{CE} =10V, Ic=0.2A	10		60	
	h _{FE (2)}	V _{CE} =5V, Ic=1.5A	5		40	
Collector-Emitter Saturation Voltage	VCE (cat)	I _C =0.4A, I _B =0.1A			0.5	V
		I _C =1.5A, I _B =0.5A			2	V
Base-Emitter Saturation Voltage	V _{BE (sat)}	I _C =0.8A, I _B =0.2A			1.2	V
Fall Time	tf	I _C =1A, I _{B1} = -I _{B2} = 0.2A			0.7	μs
Storage Time	ts	I _C =1A, I _{B1} = -I _{B2} = 0.2A			4	μs
Cut-off Frequency	f _T	V _{CE} =10V, Ic=0.1A	4			MHz

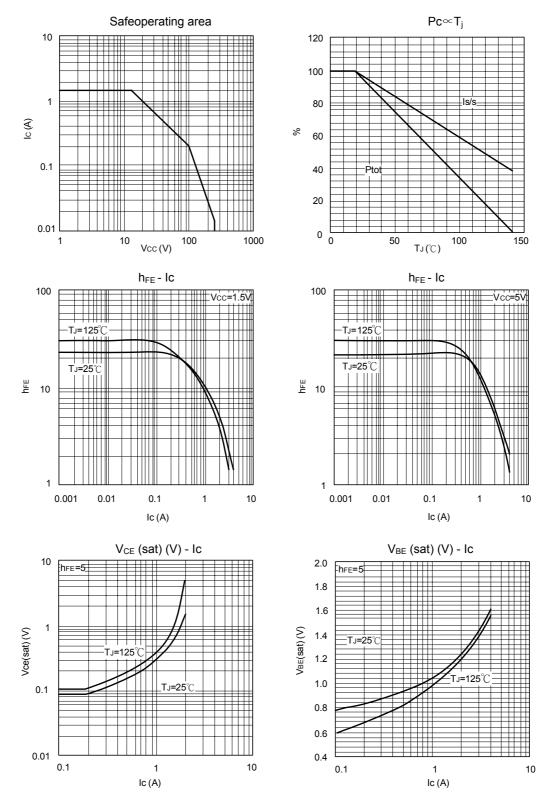
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NPN TRIPLE DIFFUSED SILICON TRANSISTOR

CHARACTERISTICS CURVES



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